

JAD10

File : JAPIO

SS Results

1	2829	IP
2	12328	PACKET+
3	169926	FILTER+
4	9611	TRAFFIC+
5	3905	ATTRIBUTE#
6	6128	RULE#
7	35	DERIV+ AND ATTRIBUTE#
8	1722	EFFICIEN+ (3D) FILTER+
9	301	2 AND 3
10	3	9 AND (5 OR 6)
11	3	9 AND (7 OR 8)
12	0	(10 AND 11)
13	1	1 AND (10 OR 11)
14	0	(10 OR 11) AND (DERIV+ (3D) ATTRIBUTE#)
15	3	(10 OR 11) AND (8)
16	1	12 OR 13

Search statement 17

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1/1 JAPIO - (C) JPO

PN - JP 11308272 A 19991105 [JP11308272]

TI - PACKET COMMUNICATION CONTROL SYSTEM AND PACKET COMMUNICATION
CONTROLLER

IN - FUJIWARA HIROYA

PA - TOSHIBA CORP

AP - JP11346798 19980423 [1998JP-0113467]

IC1 - H04L-012/56

AB - PROBLEM TO BE SOLVED: To generate filtering rules in response to the
access authority.

- SOLUTION: The system is provided with a user database 7 where access
rang information of each user is in cross reference with user
identification information and an IP address corresponding to one
client to two users is in cross reference with the user identification
information, a filtering rule generating means 8 that references the
user database 7 to generate filtering rule information to control
propriety of passing of a packet sent to a network based on a
corresponding access range information based on the user
identification information on an access request from the user to the
network, and a filtering processing means 12 that controls the
propriety of passing of the packet sent by the user by using the
filtering rule information generated by the filtering rule generating
means 8.

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Search statement 17

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** SS 17: Results 3

Search statement 18

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1/3 JAPIO - (C) JPO

PN - JP 2000151671 A 20000530 [JP2000151671]

TI - REPEATER WITH MULTI-CAST FILTERING

IN - KIYO RI

PA - HITACHI CABLE LTD

AP - JP31787498 19981109 [1998JP-0317874]

IC1 - H04L-012/44

IC2 - H04L-012/18 H04L-012/56

AB - PROBLEM TO BE SOLVED: To provide a repeater with multi-cast filtering where the utilizing efficiency of a filtering database FDB is enhanced while coping with possibility of many addresses with small setting items.

- SOLUTION: A mask M that validates only desired bits in a multi-cast address, a multi-cast address U designated by the user having user designation information in valid bits and a relay port P are registered in correspondence with each other, a destination address A of a multi-cast packet to be received is processed by using the mask M and when the processing result is coincident with the user designation multi-cast address U, the received multi-cast packet is relayed from the relay port P.

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2/3 JAPIO - (C) JPO

PN - JP 10242976 A 19980911 [JP10242976]

2/3 JAPIO - (C) JPO

PN - JP 10242976 A 19980911 [JP10242976]

TI - COMMUNICATION MONITOR AND CONTROL SYSTEM FOR ATM EXCHANGE

IN - MATSUMOTO SHINICHI; WAKAMOTO MASAOKI

PA - FUJITSU LTD

AP - JP04115497 19970225 [1997JP-0041154]

IC1 - H04L-012/28

IC2 - H04Q-003/00

AB - PROBLEM TO BE SOLVED: To attain an asynchronous transfer mode(ATM) communication monitor and control system capable of improving the secrecy and reliability of communication in executing information communication by a master protocol through an ATM exchange and efficiently executing filtering processing as to an ATM communication monitor and control system for improving the secrecy and reliability of communication.

- SOLUTION: An ATM exchange 100 is provided with a cell copying part 110 for copying an ATM cell in the case of receiving signaling from a terminal and a filtering processing part 120 for referring to the communication contents of a master protocol and, when the contents are not previously allowed contents, disconnecting its connection. When receiving signaling from the terminal, the cell copying part 110 sets up the connection and then copies and sends an ATM cell. The processing part 120 generates one packet of the master protocol from the copied and sent ATM cell, monitors the communication contents of a frame, and when the contents are not previously allowed contents, disconnects the connection.

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3/3 JAPIO - (C) JPO

PN - JP 01085442 A 19890330 [JP01085442]

TI - ADDRESS DISCRIMINATING DEVICE

IN - OTERU YOICHI

PA - NEC CORP

AP - JP24304187 19870928 [1987JP-0243041]

IC1 - H04L-011/00

AB - PURPOSE: To attain an efficient study with the filtering by a destination address and a sender address at high speed by revising the content of a content readout memory and switching a function of two sets of content readout memories when the content change is required.

- CONSTITUTION: A destination address part of a received packet is inputted to a reception buffer 13 and given to a content readout memory 15 being a filtering memory at the same time and discrimination is applied. The sender address part of incoming packet is given to the reception buffer 13 and the sender address is inputted to the memory 16 to discriminate the coincidence and if necessary, the content of the study memory is updated, switches 19, 20 are changed over immediately to switch the filtering memory and the study memory. Thus, the filtering by the destination address and the study by the sender address are implemented efficiently at high speed by using two sets of the content readout memories.

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